

FIG. 1 is a block diagram of a video processing system 100. The system 100 includes a video input 101, a video to pixel translation logic 108, a row control logic 104, a column control logic 106, a pixel array 102, and a DAC array 110. The video input 101 is connected to the video to pixel translation logic 108. The video to pixel translation logic 108 is connected to the row control logic 104 and the column control logic 106. The row control logic 104 is connected to the pixel array 102. The column control logic 106 is connected to the pixel array 102. The pixel array 102 is connected to the DAC array 110. The DAC array 110 includes DACs 111 through 117. The DAC array 110 is connected to the row control logic 104 and the column control logic 106.

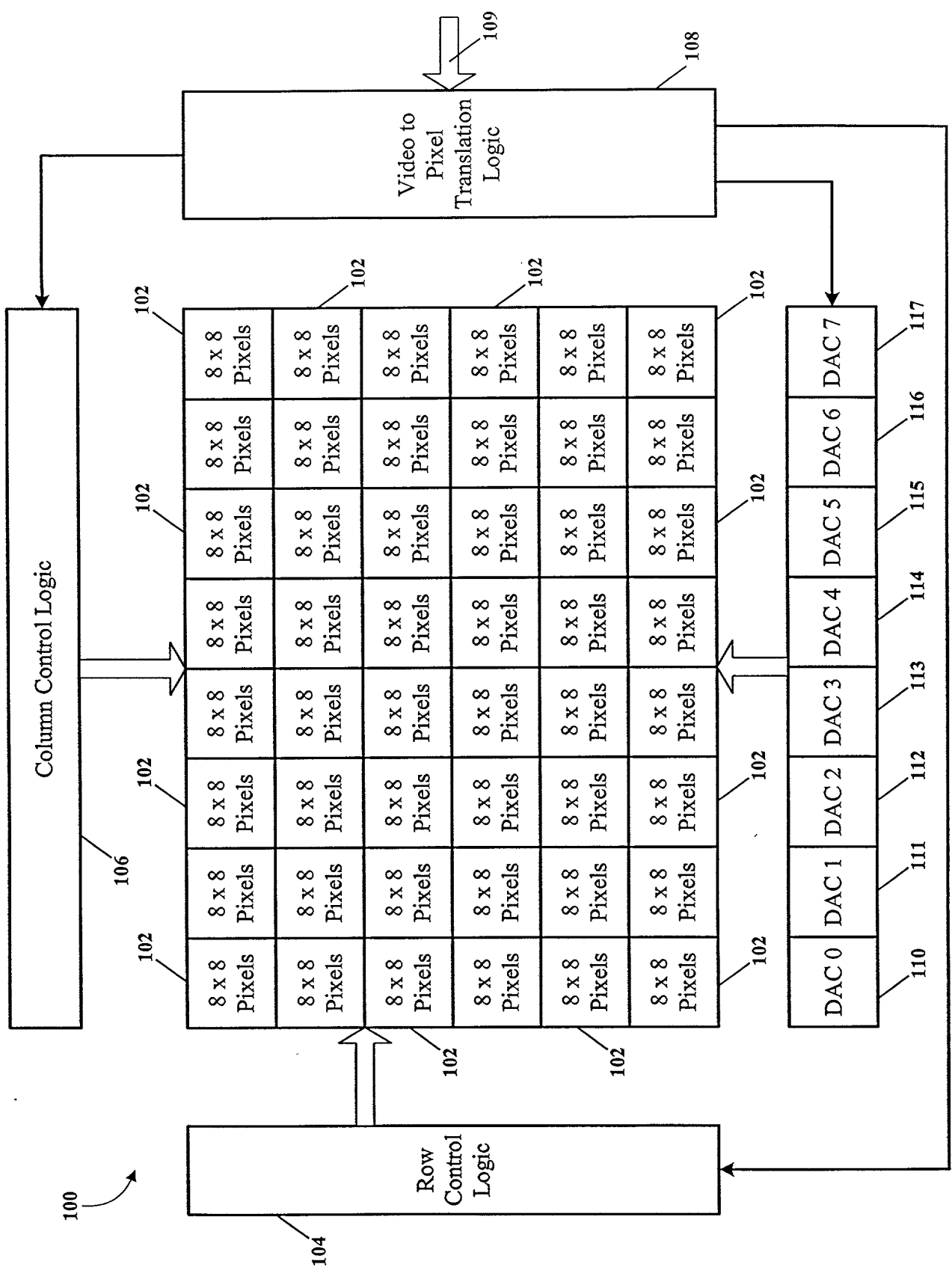


FIGURE 1

FIG. 2 is a block diagram of a pixel array 102, showing a grid of pixel elements. The array is controlled by Row Control Logic 104 and Column Control Logic 106. The array is organized into rows (Row 0 to Row 7) and columns (Column 0 to Column 7). Each pixel element (e.g., P 0,0, P 0,1, ..., P 7,7) is connected to a row line (e.g., 300, 301, ..., 377) and a column line (e.g., 180, 182). The row lines are controlled by Row Control Logic 104, and the column lines are controlled by Column Control Logic 106. The array is also connected to DACs (DAC0, DAC1, ..., DAC7) and pixel groups (To Next 8 x 8 Pixel Group).

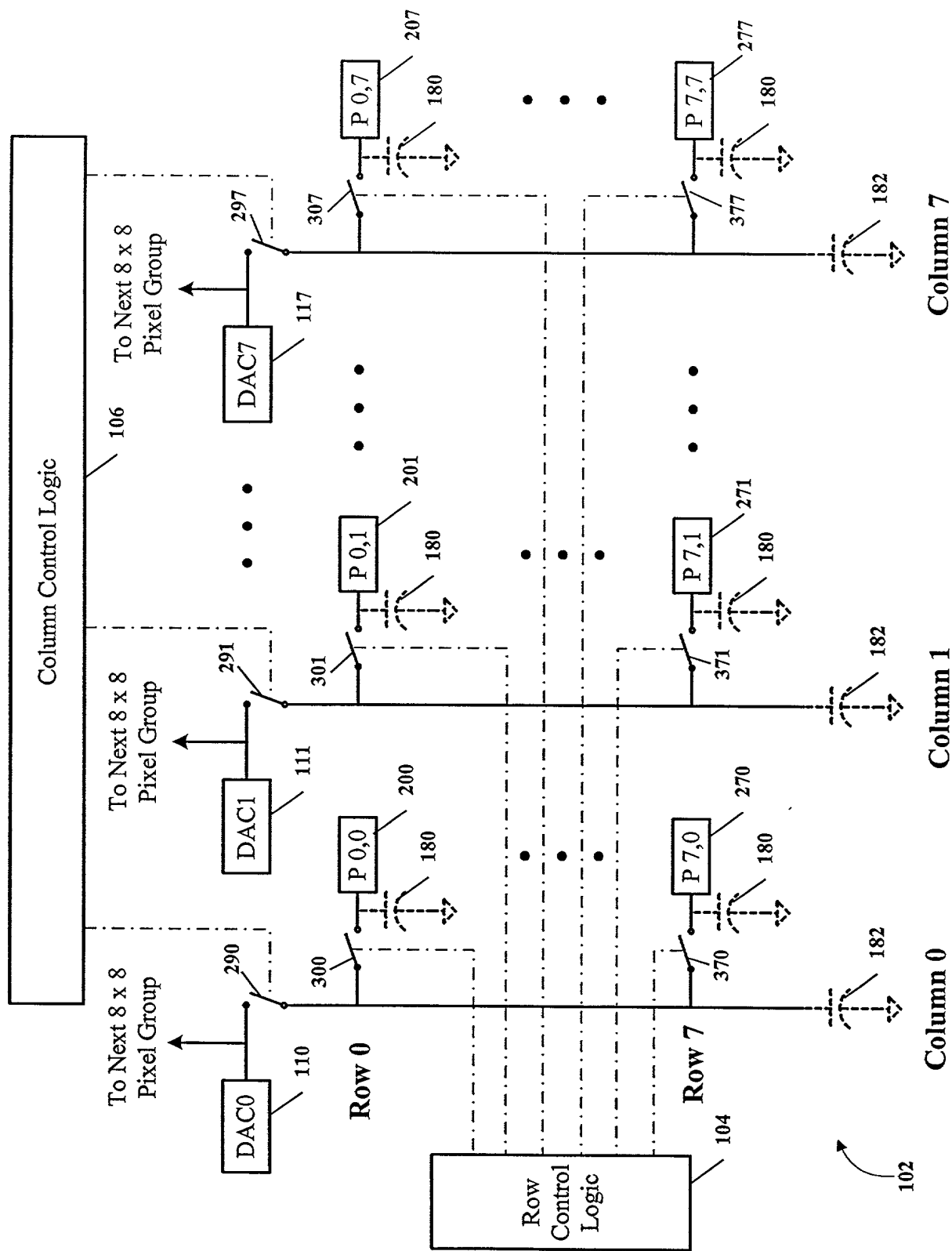


FIGURE 2

FIG. 3 is a schematic diagram of a pixel array circuit. The circuit includes a row of DACs (110, 111, 117) and a column of pixel elements (P 0,0, P 0,1, ..., P 7,7). The row elements are connected to the column elements via a series of switches (290, 291, 297, 300, 301, 307, 370, 371, 377). The circuit is divided into three sections: 'To Next 8 x 8 Pixel Group', 'To Next 8 x 8 Pixel Group', and 'To Next 8 x 8 Pixel Group'. The first section is labeled 'To Next 8 x 8 Pixel Group' and the second section is labeled 'To Next 8 x 8 Pixel Group'. The third section is labeled 'To Next 8 x 8 Pixel Group'.

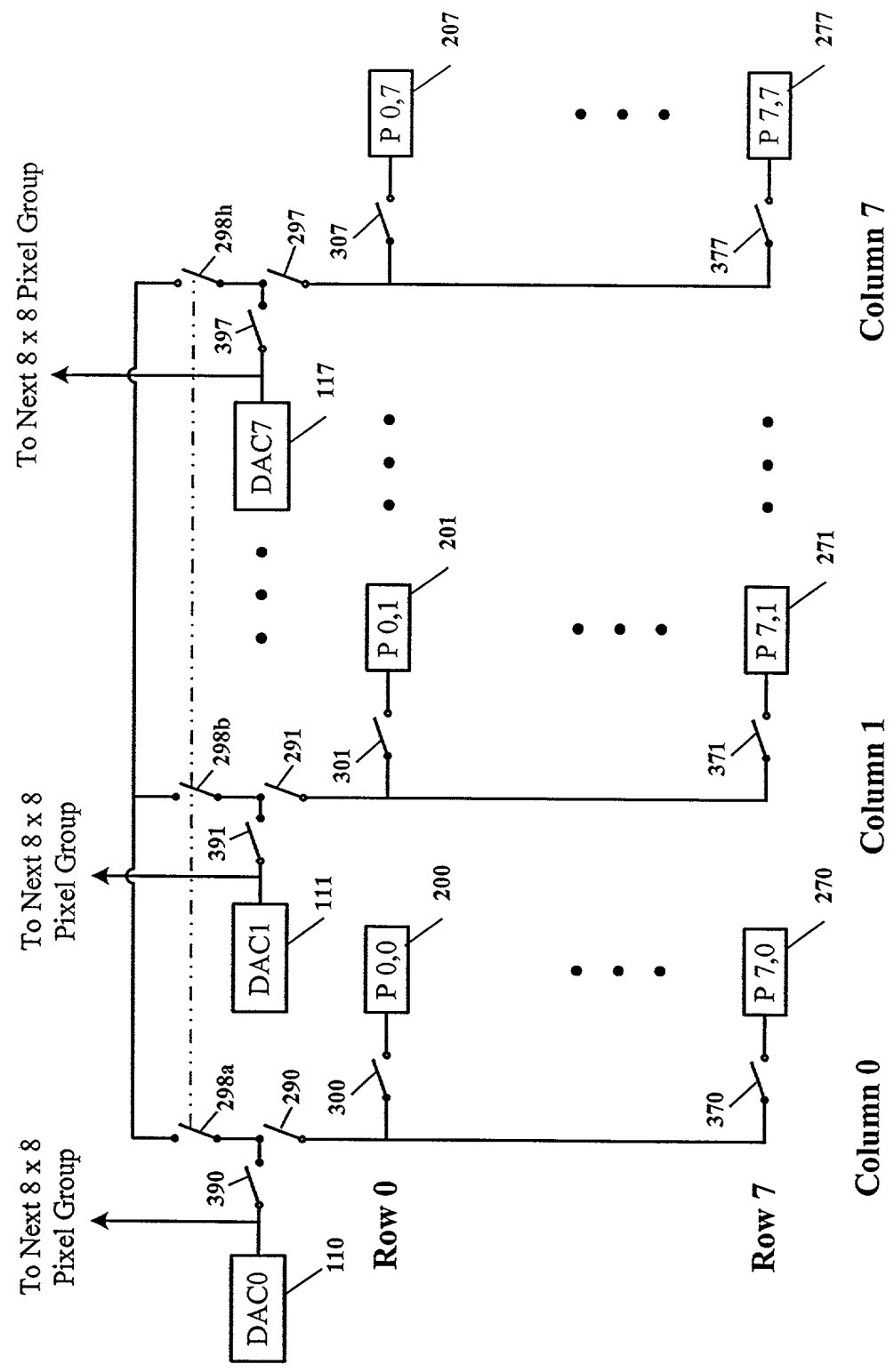


FIGURE 3

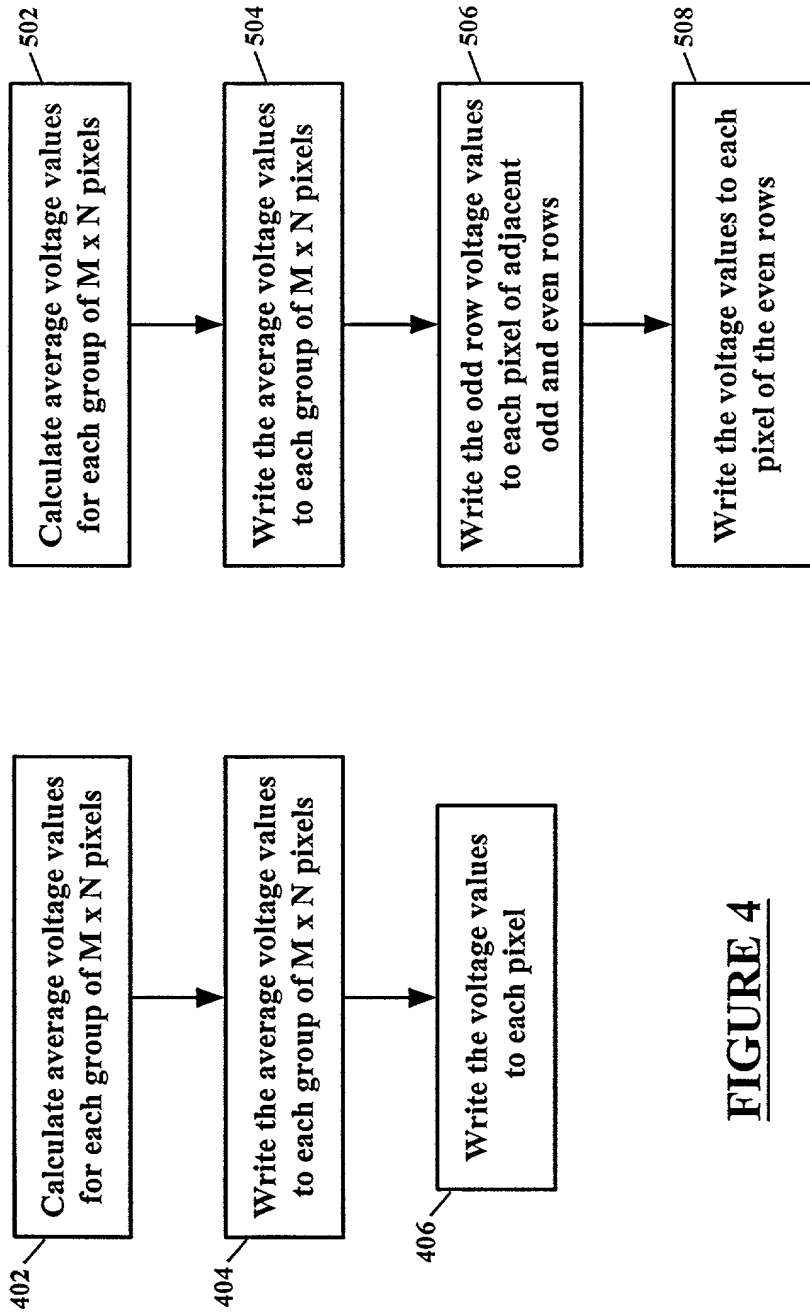


FIGURE 4

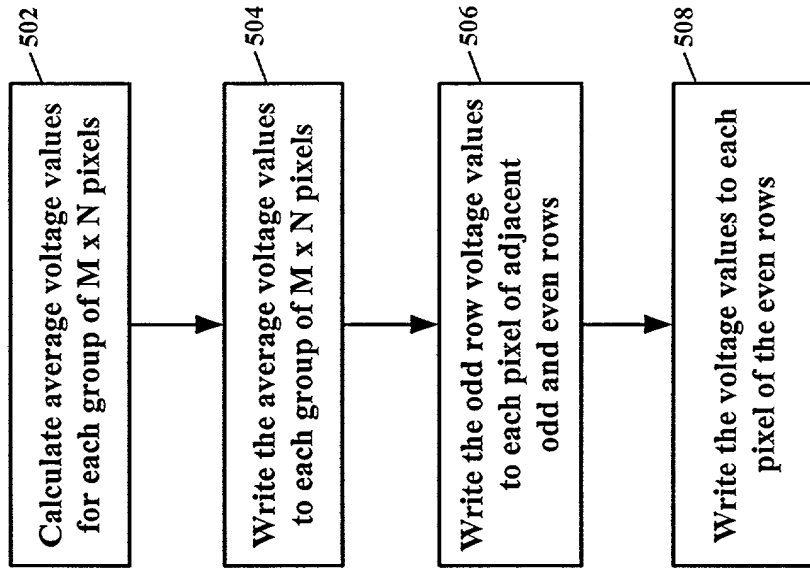


FIGURE 5

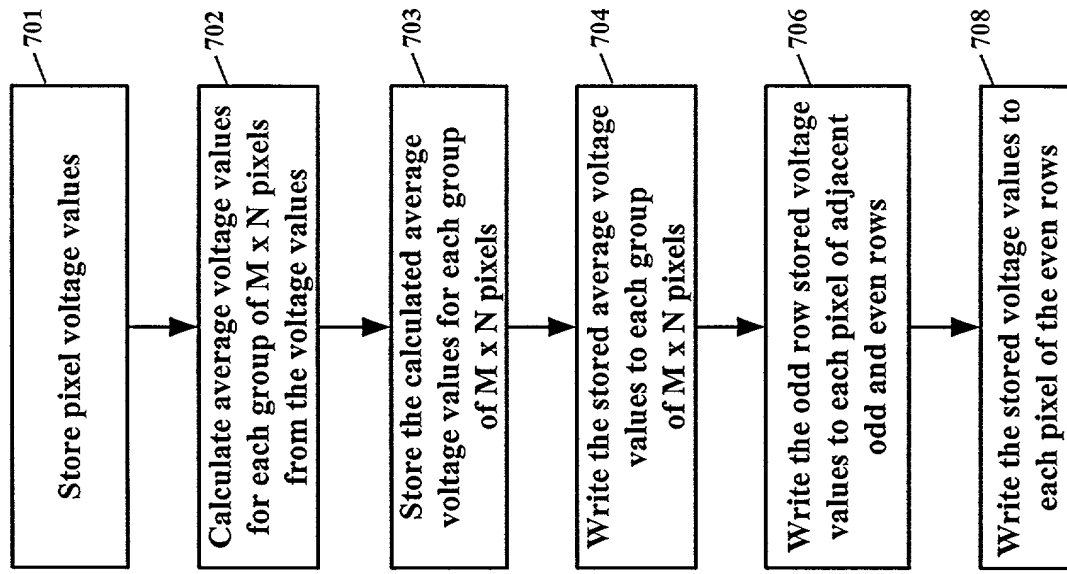


FIGURE 6

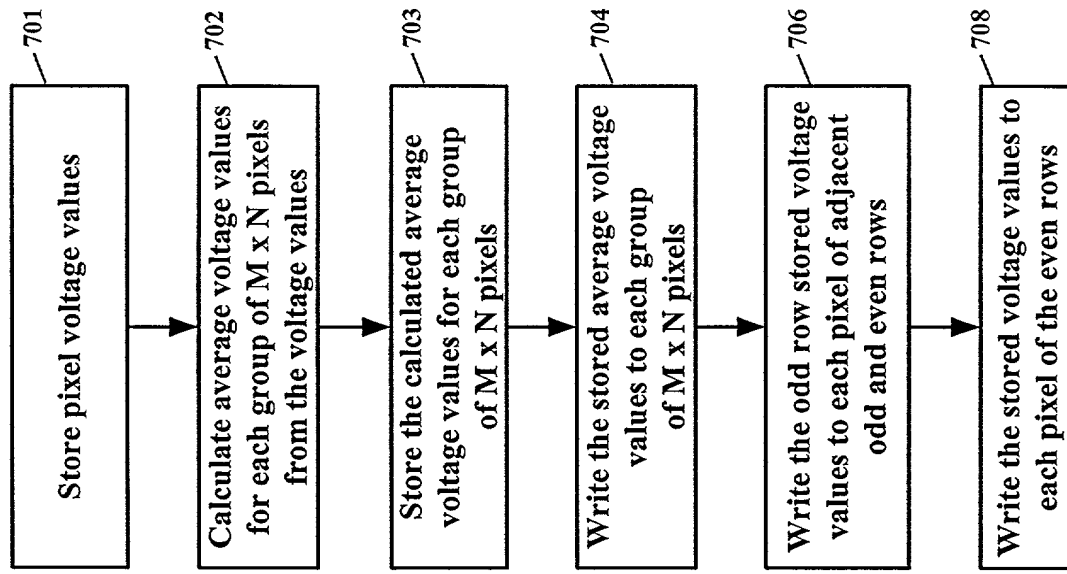


FIGURE 7

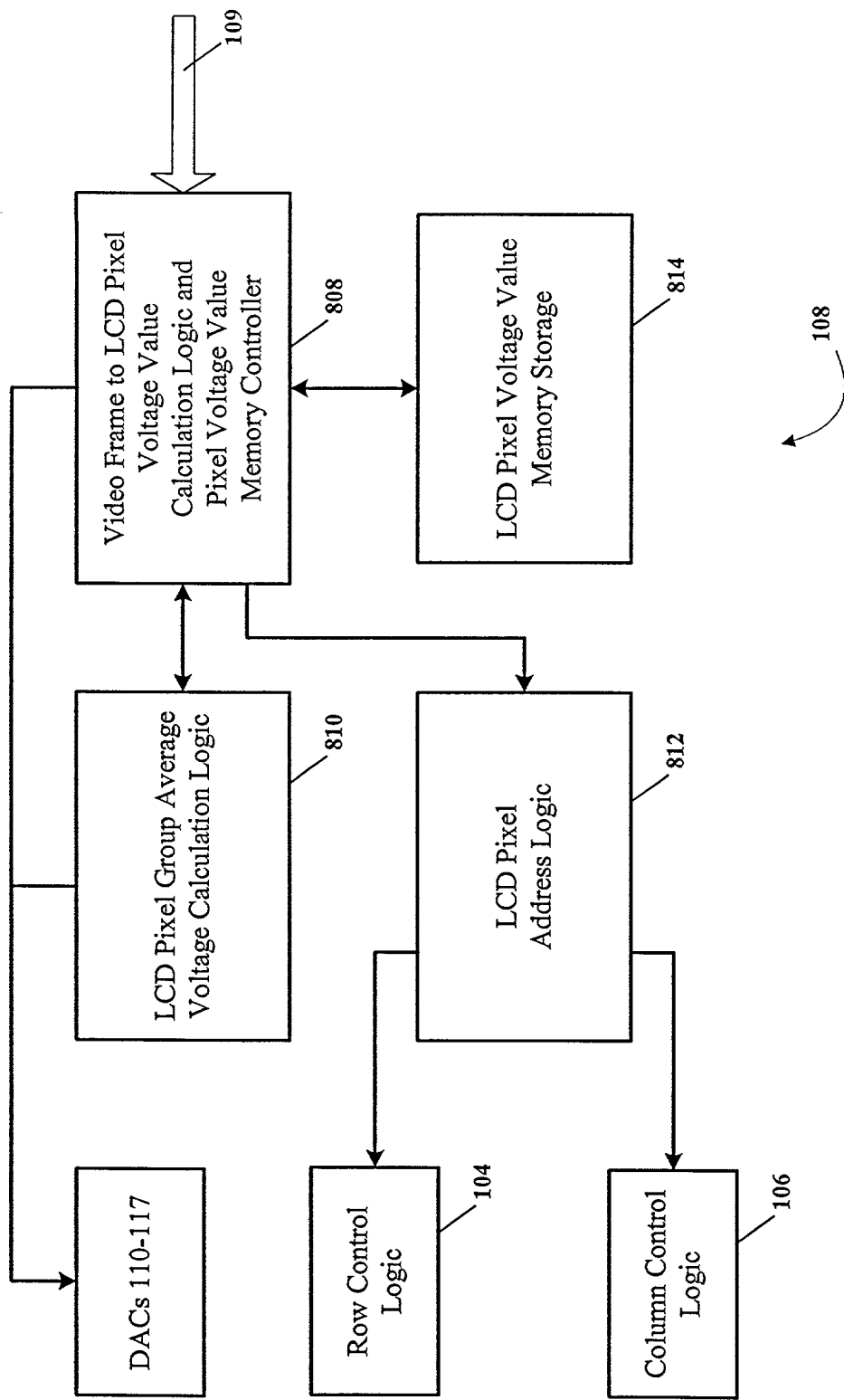


FIGURE 8